

AT32F415 AT32F421 ERTC Tamper Detection

Introduction

This example code demonstrates how to use ERTC temper detection function in AT32F415 and AT32F421.

Note: This sample code is written based on Artery's V2.x.x BSP. For other versions of BSP, users should pay attention to the differences in use.

Applicable products:

Product series	AT32F415xx
	AT32F421xx

List of major peripherals used:

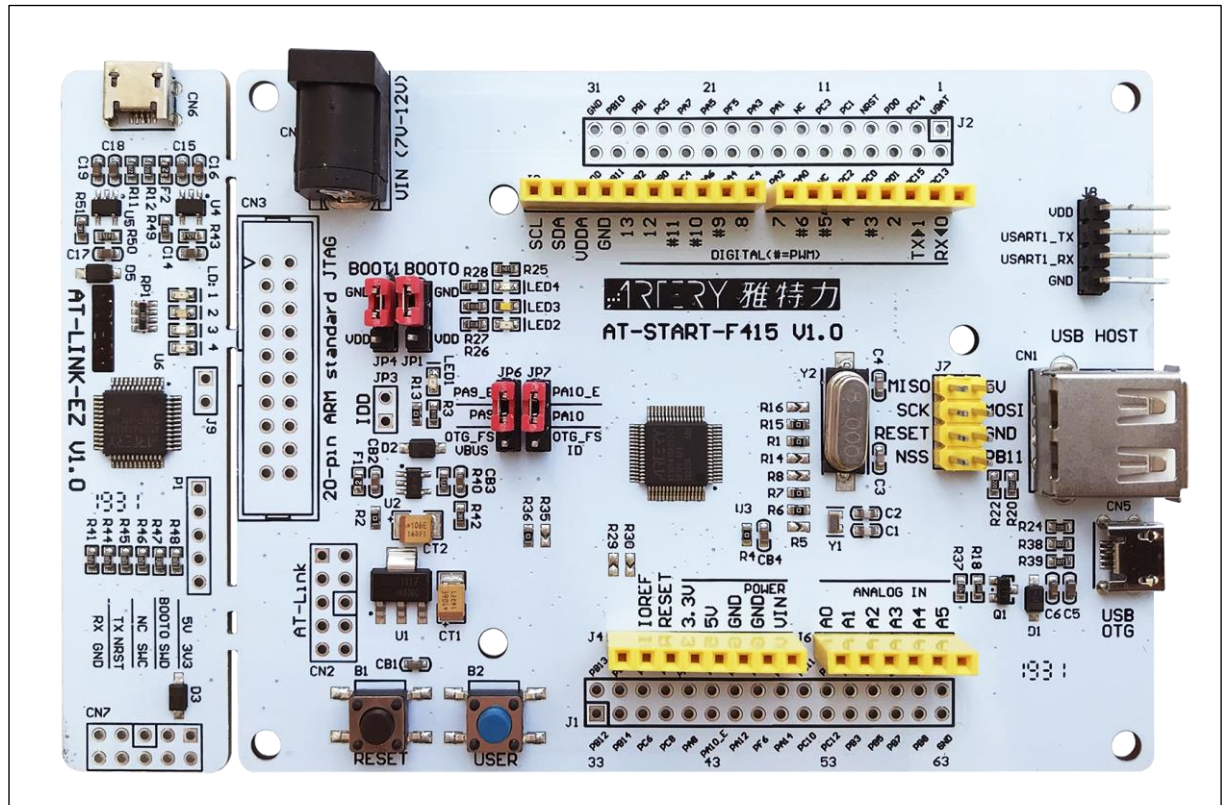
Peripherals	ERTC
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1 Quick start

1.1 Hardware resources

- 1) AT-START-F415 V1.0 and AT-START-F421 V1.0 evaluation boards
- 2) Serial interfaces: PA9, PA10

Figure 1. AT-START-F415 V1.0 evaluation board



1.2 Software resources

- 1) SourceCode
 - ERTC_Tamper

Note: All of projects are built based on Keil 5. For the need to run them in other compiling environments, user can make simple adjustments according to AT32xxx_Firmware_Library_V2.x.x\project\at_start_xxx\templates.

1.3 Example case

- 1) Open “ERTC_Tamper”, compile and download it to the evaluation board
- 2) If AT-START-F421 V1.0 board is to be used, please choose AT_START_421 project
- 3) Changing the level on PC13, when a rising edge is detected, a tamper detection is triggered. Tamper event is printed via serial interfaces upon a tamper event.

Figure 2. Print tamper event

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init: bpr data registers are not reset
tamper: bpr data registers are cleared
tamper: bpr data registers are cleared
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2 Revision history

Table 1. Document revision history

Date	Revision	Changes
2021.12.20	2.0.0	Initial release

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